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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Anand Murthy et al.	§	Art Unit: 2813
	§	
Serial No.: 10/780,826	§	Examiner: Stephen W. Smoot
	§	
Filed: February 17, 2004	§	Atty Docket: ITL.1064US
	§	P18031
For: Fabricating Strained Channel	§	
Epitaxial Source/Drain	§	Assignee: Intel Corporation
Transistors	§	

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed September 20, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: November 16, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450..

Cynthia L. Hayden
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